

Srinivasa **INSTITUTE OF TECHNOLOGY & SCIENCE**

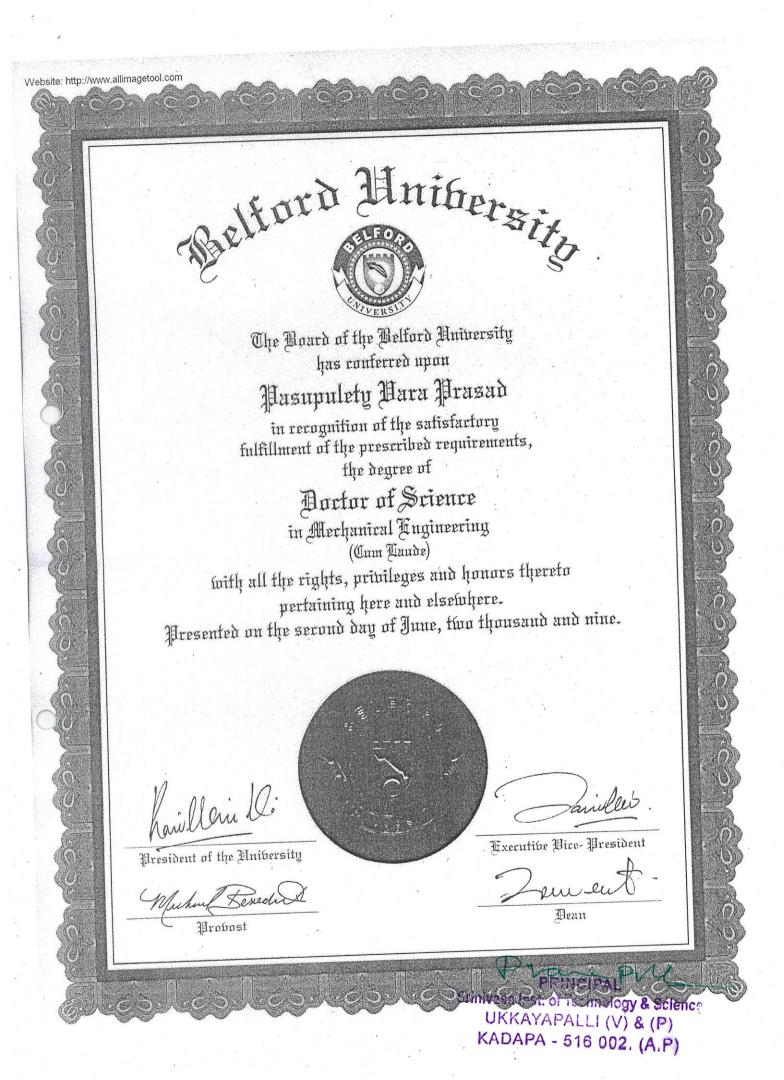
Approved by AICTE, New Delhi, Affiliated to JNT University, Anantapur)

Ukkayapalli Village, Hyderabad - Chennai Bypass Road, KADAPA - 516 002 (A.P.) e-mail: srts99@gmail.com website: www.sitskadapa.ac.in Ph: & Fax: 08562-221001

LIST OF FULL TIME TEACHERS WITH Ph.D-2020-2021

LIST G. T.			VI CONTROL OF THE CON		1
	S.NO.	EMPLOYEE NAME & DESIGNATION	QUALIFICATION	DESIGNATION	DEPARTMENT
-			M.TECH., Ph.D	PRINCIPAL	MECHANICAL ENGG.
		DI.P VARATIONS		PROFESSOR	CSE
	2	Dr ANIL BADARLA	M. TECH., FILD	,	
	3	Dr.CH VENKATA SIVA PRASAD	M.TECH., Ph.D	PROFESSOR	ECE
			M.TECH., Ph.D	PROFESSOR	EEE
9	4	Dr RAJ SELAN	IVI. I LCI II.) I III.D		

Srinivasa Inst. of Technology & Science UKKAYAPALLI (V) & (P) KADAPA - 516 002. (A.P)



Enrollment No. 15SRU/S1044



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सन्गईज विश्वविद्यालय, अलवर SunRise University, Alwar

Tehsil Ramgarh, District - Alwar (Rajasthan), INDIA

विद्या - वाचस्पति

(संकाय Engineering : Computer Science & Engineering

सनराईज विश्वविद्यालयः की हुएकेडेमिक काउंसिल बद्धारा यहाँ प्रमाणिताए किया जाति हैं। कि अो / सुश्री / श्रीमती अपना आपिता के अपित के अपना किया के आपित के आपित के अपना के अपना के आपित के अपना क

शोधप्रबन्ध का विषय

"Study on Prevention of Malicious Transaction and Privacy Preserving in Data Base System Using Trusted Oracle Architecture"

Doctor of Philosophy

(Faculty of Engineering: Computer Science & Engineering

This is to certify by the Academic Council of SunRise University that

Mr./Ms./Smt. Anil Badarla

obtained the Degree of Bottor of Philosophy after approval of the thesis.

Title of Thesis

"Study on Prevention of Malicious Transaction and Privacy Preserving in Sur Data Base System Using Trusted Oracle Architecture is University Sur Data Base System Using Trusted Oracle Architecture is University Sur

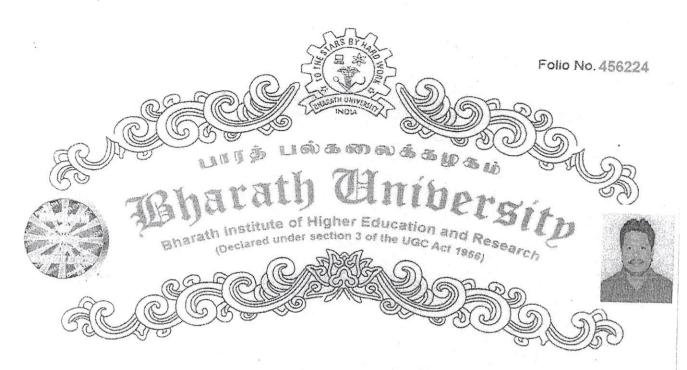
Awarded under the seal & Signature of the SunRise University on year 2018

Registrar



President

Srinivasa Inst. of Technology & Science
UKKAYAPALLI (V) & (P)
KADAPA - 516 002. (A.P)

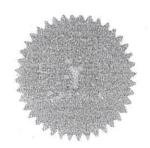


FACULTY OF ENGINEERING

The hoard of management of the Pharath University here by makes known that RAJSELVAN C born on 22-JULY-1975 has been admitted to the DOCTOR OF PHILOSOPHY in ELECTRICAL AND ELECTRONICS, he/she having satisfactory completed the prescribed programme of study and having been certified by duly appointed examiner to have qualified to receive the same at the Examination held on JUNE 2015 with Registration Number D11EE004.

Thesis title:

A Novel Zone Division Approach for Power System Fault Detection Using ANN-Based Pattern Recognition Technique



Given under the Seal of the University

DATE 173 Agaram Road

CONTROLLER OF

Srinivasa Inst. of Technology & Science